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## TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING REJECTION OVER A PENDING "REFERENCE" APPLICATION

Docket Number 042978

In re Application of: Yasuhiro WAKIZAWA et al.

Application No.: 10/514,421

Filed: November 16, 2004

For: METHOD FOR FORMING MULTILAYER CIRCUIT STRUCTURE AND BASE HAVING MULTILAYER CIRCUIT STRUCTURE

The owner\*, (1) Japan Science and Technology Agency, (2) Toppan Printing Co., Ltd., and (3) Zeon Corporation, of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application which would extend beyond the expiration date of the full statutory term of any patent granted on pending reference Application Number 10/487,997, filed on February 27, 2004, as such term is defined in 35 U.S.C. 154 and 173, and as the term of any patent granted on said reference application may be shortened by any terminal disclaimer filed prior to the grant of any patent on the pending reference application. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and any patent granted on the reference application are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

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